
Informations about the currently used Windows CE version

OS Version:

Windows CE 4.21 Build 1088
OEM Info: Dell Axim X50 Platform Type: PocketPC

Prozessor:

ARM Processor
Processor level : 4
Processor revision: 4

Memory:

Page size: 4kB
Alloc. Granularity: 64kB
Min. Appl. Address: 64kB
Max. Appl. Address: 7FFFFFFF = 2.00GB

Total physical memory: 28.84MB
Avail physical memory: 8.37MB
Total virtual memory for one process/slot: 32.00MB
Avail virtual memory for one process/slot: 28.88MB

ObjectStore

Avail.to caller: 17.38MB
Total num. bytes: 32.80MB
Total num. free: 17.38MB
Currently used : 15.42MB

Windows CE Image is in Full Kernel Mode

Identify CPU

CPUID = 0x69054114 PXA270 CPU Step C0
CPU is in Turbo Mode.

ICP:

ICCR0(0x40800000): 00000000
ICCR1(0x40800004): 00000000
ICCR2(0x40800008): 0000000C
ICDR (0x4080000C): 00000000
ICSR0(0x40800014): 00000000
ICSR1(0x40800018): 00000008
ICFOR(0x4080001C): 00000000

RTC:

RCNR(0x40900000): 680E06CF
RTAR(0x40900004): 6812B981
RTSR(0x40900008): 0000000C
RTTR(0x4090000C): 80007FFF
RDCR(0x40900010): 004F4953
20:37:19 DayOfWeek:7 WeekOfMonth: 4
RYCR(0x40900014): 000FA096
22.04.2000
RDAR1(0x40900018): 00000000
RYAR1(0x4090001C): 00000000
RDAR2(0x40900020): 00000000
RYAR2(0x40900024): 00000000
SWCR(0x40900028): 00000000
SWAR1(0x4090002C): 00000000
SWAR2(0x40900030): 00000000
RTCPICR(0x40900034): 00000000

PIAR(0x40900038): 00000000

OS Timer:

OSMR0(0x40A00000): 4D0EE233
OSMR1(0x40A00004): 4CF5760B
OSMR2(0x40A00008): 00000000
OSMR3(0x40A0000C): 00000000
OSCR (0x40A00010): 4D0F6123
OSSR (0x40A00014): 00000000
OWER (0x40A00018): 00000000
OIER (0x40A0001C): 00000003
OSNR (0x40A00020): 00000000
OSCR4(0x40A00040): 00000000
OSCR5(0x40A00044): 00000000
OSCR6(0x40A00048): 00000000
OSCR7(0x40A0004C): 00000000
OSCR8(0x40A00050): 00000000
OSCR9(0x40A00054): 00000000
OSCR10(0x40A00058): 00000000
OSCR11(0x40A0005C): 00000000
OSMR4(0x40A00080): 00000000
OSMR5(0x40A00084): 00000000
OSMR6(0x40A00088): 00000000
OSMR7(0x40A0008C): 00000000
OSMR8(0x40A00090): 00000000
OSMR9(0x40A00094): 00000000
OSMR10(0x40A00098): 00000000
OSMR11(0x40A0009C): 00000000
OMCR4(0x40A000C0): 00000000
OMCR5(0x40A000C4): 00000000
OMCR6(0x40A000C8): 00000000
OMCR7(0x40A000CC): 00000000
OMCR8(0x40A000D0): 00000000
OMCR9(0x40A000D4): 00000000
OMCR10(0x40A000D8): 00000000
OMCR11(0x40A000DC): 00000000

Interrupt Control:

ICIP(0x40D00000): 00000000
ICMR(0x40D00004): C6F00D1C
 USB Host 2
 USB Host 1
 Keypad Controller
 GPIO_0
 GPIO_x
 USB Client
 STUART
 BTUART
 FFUART
 MultiMediaCard
 DMA Controller
 OS Timer 0
 One Hz Clock
 Real-Time Clock Alarm
ICLR(0x40D00008): 00000000
ICFP(0x40D0000C): 00000000
ICPR(0x40D00010): 00000000
ICCR(0x40D00014): 00000001

GPIO:

GPLR0 (0x40E00000): 053D81FE
GPLR1 (0x40E00004): 01FF4002
GPLR2 (0x40E00008): 5231C000
GPDR0 (0x40E0000C): F3FB861C
GPDR1 (0x40E00010): FCEFBFBF

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GPDR2 (0x40E00014): 9207FFFF
GPSR0 (0x40E00018): 00000000
GPSR1 (0x40E0001C): 00000000
GPSR2 (0x40E00020): 00000000
GPCR0 (0x40E00024): 00000000
GPCR1 (0x40E00028): 00000000
GPCR2 (0x40E0002C): 00000000
GRER0 (0x40E00030): 00007801
GRER1 (0x40E00034): 00000000
GRER2 (0x40E00038): 00280000
GFER0 (0x40E0003C): 00005801
GFER1 (0x40E00040): 00000000
GFER2 (0x40E00044): 40380000
GEDR0 (0x40E00048): 00000000
GEDR1 (0x40E0004C): 00000000
GEDR2 (0x40E00050): 00000000
GAFR0_L(0x40E00054): 80140000
GAFR0_U(0x40E00058): 001A851A
GAFR1_L(0x40E0005C): 6000000A
GAFR1_U(0x40E00060): 0005A0AA
GAFR2_L(0x40E00064): A0000000
GAFR2_U(0x40E00068): 01500002
GAFR3_L(0x40E0006C): 5409953C
GAFR3_U(0x40E00070): 00001401
GPLR3 (0x40E00100): 01E9EFC0
GEDR3 (0x40E0010C): 007FF7C0
GPSR3 (0x40E00118): 00000000
GPCR3 (0x40E00124): 00000000
GRER3 (0x40E00130): 00000001
GFER3 (0x40E0013C): 00000801
GEDR3 (0x40E00148): 00000000

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00: Input  GPIO Low  Rising Edge Falling Edge
01: Input  GPIO High
02: Output GPIO High
03: Output GPIO High
04: Output GPIO High
05: Input  GPIO High
06: Input  GPIO High
07: Input  GPIO High
08: Input  GPIO High
09: Output AF1 Low      HZ_CLK - Real-Time 1 Hz Clock
10: Output AF1 Low      HZ_CLK - Real-Time 1 Hz Clock
11: Input  GPIO Low  Rising Edge Falling Edge
12: Input  GPIO Low  Rising Edge Falling Edge
13: Input  GPIO Low  Rising Edge
14: Input  GPIO Low  Rising Edge Falling Edge
15: Output AF2 High      nCS<1> - Static Chip Select 1
16: Output AF2 Low      PWM_OUT<0> - Pulse Width Modulation Channel 0
17: Output AF2 High      PWM_OUT<1> - Pulse Width Modulation Channel 1
18: Input  AF1 High      RDY - Variable Latency I/O Ready Pin
19: Output GPIO High
20: Output AF1 High      nSDCS<2> - SDRAM Chip Select 2
21: Output AF1 High      nSDCS<3> - SDRAM Chip Select 3
22: Output GPIO Low
23: Output AF2 Low      SSPSCLK - Synchronous Serial Port 1 Clock
24: Output AF2 High     SSPSFRM - Synchronous Serial Port 1 Frame
25: Output AF2 Low      SSPTXD - Synchronous Serial Port 1 Transmit Data
26: Input  AF1 High     SSPRXD - Synchronous Serial Port 1 Receive Data
27: Input  GPIO Low
28: Output GPIO Low
29: Output GPIO Low
30: Output GPIO Low
31: Output GPIO Low
32: Output AF2 Low      MMCLK - MultiMediaCard and SD/SDIO Card Bus Clock
33: Output AF2 High     nCS[5] - Static Chip Select 5
34: Output GPIO Low
35: Output GPIO Low
36: Output GPIO Low
37: Output GPIO Low
38: Input  GPIO Low

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39: Output GPIO Low
40: Output GPIO Low
41: Output GPIO Low
42: Output GPIO Low
43: Output GPIO Low
44: Output GPIO Low
45: Output GPIO Low
46: Input AF2 High          STD_RXD - Receive Pin for Standard UART and Slow Infrared Fu
ncions
47: Output AF1 Low          STD_TXD - Transmit Pin for Standard UART and Slow Infrared I
uncions
48: Output AF2 High          nPOE - PC Card Output Enable
49: Output AF2 High          nPWE - PC Card Write Enable
50: Output AF2 High          nPIOR - PC Card I/O Read
51: Output AF2 High          nPIOW - PC Card I/O Write
52: Input GPIO High
53: Output GPIO High
54: Output AF2 High          nPCE<2> - PC Card Enable 2
55: Output AF2 High          nPREG - PC Card Register Select
56: Input AF1 High          nPWAIT - PC Card Wait
57: Input AF1 Low           nIOIS16 - PC Card I/O Select 16
58: Output GPIO Low
59: Output GPIO Low
60: Output GPIO Low
61: Output GPIO Low
62: Output GPIO Low
63: Output GPIO Low
64: Output GPIO Low
65: Output GPIO Low
66: Output GPIO Low
67: Output GPIO Low
68: Output GPIO Low
69: Output GPIO Low
70: Output GPIO Low
71: Output GPIO Low
72: Output GPIO Low
73: Output GPIO Low
74: Output GPIO Low
75: Output GPIO Low
76: Output GPIO Low
77: Output GPIO Low
78: Output AF2 High          nCS<2> - Static Chip Select 2
79: Output AF2 High          nCS<3> - Static Chip Select 3
80: Output AF2 High          nCS<4> - Static Chip Select 4
81: Output GPIO Low
82: Output GPIO Low
83: Input GPIO Low          Rising Edge Falling Edge
84: Input GPIO High          Falling Edge
85: Input GPIO High          Rising Edge Falling Edge
86: Input GPIO Low
87: Input GPIO Low
88: Input GPIO Low
89: Output GPIO High
90: Input AF1 Low           KP_MKIN<5> - Keypad Matrix Key Input 5
91: Input AF1 Low           KP_MKIN<6> - Keypad Matrix Key Input 6
92: Output AF1 High          MMDAT<0> - MultiMediaCard Data 0
93: Input GPIO Low
94: Input GPIO High          Falling Edge
95: Output GPIO Low
96: Input GPIO Low          Rising Edge Falling Edge
97: Input AF3 Low           KP_MKIN<3> - Keypad Matrix Key Input 3
98: Input AF3 Low           KP_MKIN<4> - Keypad Matrix Key Input 4
99: Input GPIO Low
100: Input AF1 Low          KP_MKIN<0> - Keypad Matrix Key Input 0
101: Input AF1 Low          KP_MKIN<1> - Keypad Matrix Key Input 1
102: Output AF1 High          nPCE<1> - PC Card Enable 1
103: Output AF2 High          KP_MKOUT<0> - Keypad Matrix Key Output 0
104: Output AF1 High          PSKTSEL - PC Card Socket Select
105: Output AF2 High          KP_MKOUT<2> - Keypad Matrix Key Output 2
106: Output GPIO High
107: Input GPIO High          Falling Edge

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108: Output GPIO Low
109: Output AF1 High MMDAT<1> - MultiMediaCard Data 1
110: Output AF1 High MMDAT<2>/MMCCS<0> - MultiMediaCard Data 2/Chip Select 0
111: Output AF1 High MMDAT<3>/MMCCS<1> - MultiMediaCard Data 3/Chip Select 1
112: Output AF1 High MMCMD - MultiMediaCard Command
113: Output GPIO Low
114: Output GPIO Low
115: Output GPIO High
116: Output GPIO Low
117: Output AF1 High SCL - I²C Clock
118: Output AF1 High SDA - I²C Data
119: Input GPIO High
120: Input GPIO High

Clocks Manager:

CCCR(0x41300000): 02000310
Turbo: 624.00 MHz
Run: 208.00 MHz
System: 208.00 MHz
Memory: 208.00 MHz MEMCLK: 4.8 ns
LCD: 104.00 MHz LCDCLK: 9.6 ns

CKEN(0x41300004): 00D90EA3

All PWMs
STUART Unit Clock
BTUART Unit Clock
OS Timer Unit Clock
USB Host Unit Clock
USB Client Unit Clock
LCD Controller Clock
Keypad Interface Clock
Internal Memory Clock
Memory Controller
SSP1 Unit Clock

OSCC(0x41300008): 0000000F
CCSR(0x4130000C): 30000310

LCD Controller:

LCCR0(0x44000000): 00000000
LCCR1(0x44000004): 00000000
LCCR2(0x44000008): 00000000
LCCR3(0x4400000C): 04F00001
LCCR4(0x44000010): 00000000
LCCR5(0x44000014): 00000000
FBR0(0x44000020): 00000000
FBR1(0x44000024): 00000000
FBR2(0x44000028): 00000000
FBR3(0x4400002C): 00000000
FBR4(0x44000030): 00000000
LCSR1(0x44000034): 00000000
LCSR0(0x44000038): 00000000
LIIDR (0x4400003C): 40000410
TRGBR (0x44000040): 00AA5500
TCR (0x44000044): 0000754F
OVL1C1(0x44000050): 00200000
OVL1C2(0x44000060): 00000000
OVL2C1(0x44000070): 00200000
OVL2C2(0x44000080): 00000000
CCR(0x44000090): 00000000
CMDCR(0x44000100): 00000100
PRSR(0x44000104): 00000000
FBR5(0x44000110): 00000000
FBR6(0x44000114): 00000000
FDADR0(0x44000200): 00000000
FSADR0(0x44000204): 00000000
FIDR0 (0x44000208): 00000000
LDCMD0(0x4400020C): 00000000

FDADR1(0x44000210): 00000000
FSADR1(0x44000214): 00000000
FIDR1 (0x44000218): 00000000
LDCMD1(0x4400021C): 00000000
FDADR2(0x44000220): 00000000
FSADR2(0x44000224): 00000000
FIDR2 (0x44000228): 00000000
LDCMD2(0x4400022C): 00000000
FDADR3(0x44000230): 00000000
FSADR3(0x44000234): 00000000
FIDR3 (0x44000238): 00000000
LDCMD3(0x4400023C): 00000000
FDADR4(0x44000240): 00000000
FSADR4(0x44000244): 00000000
FIDR4 (0x44000248): 00000000
LDCMD4(0x4400024C): 00000000
FDADR5(0x44000250): 00000000
FSADR5(0x44000254): 00000000
FIDR5 (0x44000258): 00000000
LDCMD5(0x4400025C): 00000000
LCDBSCNTR(0x48000054): 00000005

Memory Controller:

MDCNFG(0x48000000): 0ACB2BCC

Partition 2 at A800.0000 enabled

Partition 3 at AC00.0000 enabled

32 bits

9 columns, 13 rows, 4 internal banks

Total DRAM Size: 26 bits = 64 MB

MDREFR(0x48000004): 201EE030

MSC0 (0x48000008): 25D225D2

Static Chip Select 0 at 0000.0000:

Burst-of-four ROM or burst-of-four Flash (with non-burts writes)

ROM bus width: 32 bits

ROM delay first access: 20 Clks, 96.2ns

ROM delay next access: 5 Clks, 24.0ns

ROM/SRAM recovery time: 5 Clks, 24.0ns

Slower Device

Static Chip Select 1 at 0400.0000:

Burst-of-four ROM or burst-of-four Flash (with non-burts writes)

ROM bus width: 32 bits

ROM delay first access: 20 Clks, 96.2ns

ROM delay next access: 5 Clks, 24.0ns

ROM/SRAM recovery time: 5 Clks, 24.0ns

Slower Device

MSC1 (0x4800000C): 24842481

Static Chip Select 2 at 0800.0000:

SRAM

ROM bus width: 32 bits

ROM delay first access: 8 Clks, 38.5ns

ROM delay next access: 4 Clks, 19.2ns

ROM/SRAM recovery time: 5 Clks, 24.0ns

Slower Device

Static Chip Select 3 at 0C00.0000:

Variable Latency I/O (VLIO)

ROM bus width: 32 bits

ROM delay first access: 8 Clks, 38.5ns

ROM delay next access: 4 Clks, 19.2ns

ROM/SRAM recovery time: 5 Clks, 24.0ns

Slower Device

MSC2 (0x48000010): 7FF47FFC

Static Chip Select 4 at 1000.0000:

Variable Latency I/O (VLIO)

ROM bus width: 16 bits

ROM delay first access: 30 Clks, 144.2ns

ROM delay next access: 30 Clks, 144.2ns

ROM/SRAM recovery time: 15 Clks, 72.1ns

Slower Device

Static Chip Select 5 at 1400.0000:

Variable Latency I/O (VLIO)
ROM bus width: 32 bits
ROM delay first access: 30 Clks, 144.2ns
ROM delay next access: 30 Clks, 144.2ns
ROM/SRAM recovery time: 15 Clks, 72.1ns
Slower Device

MECR (0x48000014): 00000003
SXCNFG(0x4800001C): 40046013
MCMEM0(0x48000028): 00020508
Address Setup before Command Assertion:
10 Clks, 48.1ns
Command Assertion Time:
Reads: 36 Clks, 173.1ns Writes: 35 Clks, 168.3ns
Wait Time before checking for nPWAIT:
12 Clks, 57.69ns
Command Assertion Time after nPWAIT:
Reads: 24 Clks, 115.38ns Writes: 23 Clks, 110.58ns
Address Hold after Command Deassertion:
8 Clks 38.5ns

MCMEM1(0x4800002C): 00020508
Address Setup before Command Assertion:
10 Clks, 48.1ns
Command Assertion Time:
Reads: 36 Clks, 173.1ns Writes: 35 Clks, 168.3ns
Wait Time before checking for nPWAIT:
12 Clks, 57.69ns
Command Assertion Time after nPWAIT:
Reads: 24 Clks, 115.38ns Writes: 23 Clks, 110.58ns
Address Hold after Command Deassertion:
8 Clks 38.5ns

MCATT0(0x48000030): 00020E08
Address Setup before Command Assertion:
10 Clks, 48.1ns
Command Assertion Time:
Reads: 90 Clks, 432.7ns Writes: 89 Clks, 427.9ns
Wait Time before checking for nPWAIT:
30 Clks, 144.23ns
Command Assertion Time after nPWAIT:
Reads: 60 Clks, 288.46ns Writes: 59 Clks, 283.65ns
Address Hold after Command Deassertion:
8 Clks 38.5ns

MCATT1(0x48000034): 00020E08
Address Setup before Command Assertion:
10 Clks, 48.1ns
Command Assertion Time:
Reads: 90 Clks, 432.7ns Writes: 89 Clks, 427.9ns
Wait Time before checking for nPWAIT:
30 Clks, 144.23ns
Command Assertion Time after nPWAIT:
Reads: 60 Clks, 288.46ns Writes: 59 Clks, 283.65ns
Address Hold after Command Deassertion:
8 Clks 38.5ns

MCIO0 (0x48000038): 00020510
Address Setup before Command Assertion:
18 Clks, 86.5ns
Command Assertion Time:
Reads: 36 Clks, 173.1ns Writes: 35 Clks, 168.3ns
Wait Time before checking for nPWAIT:
12 Clks, 57.69ns
Command Assertion Time after nPWAIT:
Reads: 24 Clks, 115.38ns Writes: 23 Clks, 110.58ns
Address Hold after Command Deassertion:
8 Clks 38.5ns

MCIO1 (0x4800003C): 00020510
Address Setup before Command Assertion:
18 Clks, 86.5ns

Command Assertion Time:
Reads: 36 Clks, 173.1ns Writes: 35 Clks, 168.3ns
Wait Time before checking for nPWAIT:
12 Clks, 57.69ns
Command Assertion Time after nPWAIT:
Reads: 24 Clks, 115.38ns Writes: 23 Clks, 110.58ns
Address Hold after Command Deassertion:
8 Clks 38.5ns

MDMRS (0x48000040): 00320032
BOOT_DEF(0x48000044): 00000008
ARB_CNTL(0x48000048): 03000234
BSCNTR0(0x4800004C): FFFFFFFF
BSCNTR1(0x48000050): FFFFFFFF
LCDBSCNTR(0x48000054): 00000005
MDMRSLP(0x48000058): 00000000
BSCNTR2(0x4800005C): FFFFFFFF
BSCNTR3(0x48000060): FFFFFFFF
SA1110(0x48000064): 00000000

Flash Identifier at 0x00000000:
Manufacturer 00890089h Device ID 88108810h
Found Intel L18/L30 flash memory device 1.8V/1.8V -B 256MBit

Flash Identifier at 0x04000000:
Manufacturer 00890089h Device ID 880D880Dh
Found Intel L18/L30 flash memory device 1.8V/1.8V -T 256MBit